

706
JL

1-0972 U.S. PTO
09/759219
01/16/01

Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>RW PD3</u> Q.A. <u>TC</u>	PATENT DATE
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APPLICATION NO. 09/759219	CONT/PRIOR F	CLASS 375 370	SUBCLASS 470.00	ART UNIT 2534	EXAMINER K. DING Vanderpape
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APPLICANTS

Shinya Muraoka

TITLE

DC-offset eliminating method and receiving circuit

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
370	470						
INTERNATIONAL CLASSIFICATION							
40	4T	3116					
40	4T	3122					

☐ Continued on Issue Slip inside File Jacket

☐ TERMINAL
DISCLAIMER

DRAWINGS

CLAIMS ALLOWED

Sheets Drwg. Figs. Drwg. Print Fig. Total Claims Print Claim for O.G.*

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____

☐ The terminal _____ months of this patent have been disclaimed.

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due Date Paid

ISSUE BATCH NUMBER

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